



2123 #2 BT

PATENT

9-16-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Scott T. Becker

Application No: 10/026,245

Filed: December 17, 2001

For: MEMORIES HAVING REDUCED BITLINE  
VOLTAGE OFFSETS

Group Art Unit: 2123

Examiner: Unassigned

Atty. Docket No: ARTCP012C

Date: September 4, 2002

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on September 4, 2002.

Signed: \_\_\_\_\_

Kay Harlow

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Technology Center 2100

INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR §§1.56 AND 1.97(c)

Commissioner for Patents  
Washington, DC 20231

Dear Sir:

The references listed in the attached PTO Form 1449, copies of which are attached, may be material to examination of the above-identified patent application. Applicant submits these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application.

This Information Disclosure Statement (IDS) is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is believed to be filed before the mailing date of a first Office Action on the merits. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 50-0805 (Order No. ARTCP012C).

Respectfully submitted,

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